Title: Method for manufacturing a power module with a circuit

arrangement comprising active and passive semiconductor

components

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## Abstract:

The invention relates to a method of manufacturing a power module with a circuit arrangement comprising active and passive components, and with a circuit carrier, whereby at least some of the active semiconductor components are soldered onto a DCB substrate and at least some of the passive semiconductor components are printed onto at least one ceramic carrier by means of thick-film technology. The top side of the DCB substrate is structured to form conductive strips and connecting surfaces and to accommodate the active and passive semiconductor components of the circuit arrangement. Onto the ceramic carrier for each passive component, a first printed layer and at least one contact surface as an additional printed layer laterally following the first printed layer is printed by means of thick-film technology. The ceramic carriers for the passive components in thick-film technology are connected by means of soldering with the corresponding connecting surface(s) of the DCB substrate, via the at least one contact surface.



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